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Bib Data Sheet

CONFIRMATION NO. 2986

SERIAL NUMBER 10792,396	FILING DATE 03/04/2004 RULE	CLASS 451/41	GROUP ART UNIT 3723	ATTORNEY DOCKET NO. 053848-5021
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APPLICANTS

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** CONTINUING DATA *****
 This application is a CIP of PCT/JP02/09391 09/13/2002

** FOREIGN APPLICATIONS *****
 JAPAN 2001-285318 09/19/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 05/26/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 28	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature	Initials		

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TITLE
 Processing device, processing method and method of manufacturing semiconductor device

FILING FEE RECEIVED 1044	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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